


ACADEMIC YEAR (2021-2022)

		J K K Munirajah college of Technology, T.N.Palayam			Metric No 1.3.2	
S.No	Name of the course	course code	programme offering	Contents mapping to		Number of students
				project work	internship	
(2021-2022) Regulation-2017						
1	Project Work	EC8811	Electronics and Communication Engineering	✓	✓	14
2	Electronic Circuits- I	EC8351	Electronics and Communication Engineering	✓	✓	14
3	Electronic Circuits II	EC8452	Electronics and Communication Engineering	✓		11
4	Linear Integrated Circuits	EC8453	Electronics and Communication Engineering	✓		12
5	Microprocessors and Microcontrollers	EC8691	Electronics and Communication Engineering	✓	✓	14
6	Wireless Networks	EC8004	Electronics and Communication Engineering	✓		12
7	Embedded and Real Time Systems	EC8791	Electronics and Communication Engineering	✓	✓	14
8	Satellite Communication	EC8094	Electronics and Communication Engineering	✓		12


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EC8811

PROJECT WORK

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OBJECTIVES:

- To develop the ability to solve a specific problem right from its identification and literature review till the successful solution of the same. To train the students in preparing project reports and to face reviews and viva voce examination.

The students in a group of 3 to 4 works on a topic approved by the head of the department under the guidance of a faculty member and prepares a comprehensive project report after completing the work to the satisfaction of the supervisor. The progress of the project is evaluated based on a minimum of three reviews. The review committee may be constituted by the Head of the Department. A project report is required at the end of the semester. The project work is evaluated based on oral presentation and the project report jointly by external and internal examiners constituted by the Head of the Department.

TOTAL: 300 PERIODS

OUTCOME:

- On Completion of the project work students will be in a position to take up any challenging practical problems and find solution by formulating proper methodology.



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OBJECTIVES:

- To understand the methods of biasing transistors
- To design and analyze single stage and multistage amplifier circuits
- To analyze the frequency response of small signal amplifiers
- To design and analyze the regulated DC power supplies.
- To troubleshoot and fault analysis of power supplies.

UNIT I BIASING OF DISCRETE BJT, JFET AND MOSFET 9

BJT– Need for biasing - DC Load Line and Bias Point – DC analysis of Transistor circuits - Various biasing methods of BJT – Bias Circuit Design - Thermal stability - Stability factors - Bias compensation techniques using Diode, thermistor and sensistor – Biasing BJT Switching Circuits- JFET - DC Load Line and Bias Point - Various biasing methods of JFET - JFET Bias Circuit Design - MOSFET Biasing - Biasing FET Switching Circuits.

UNIT II BJT AMPLIFIERS 9

Small Signal Hybrid π equivalent circuit of BJT – Early effect - Analysis of CE, CC and CB amplifiers using Hybrid π equivalent circuits - AC Load Line Analysis- Darlington Amplifier - Bootstrap technique - Cascade, Cascode configurations - Differential amplifier, Basic BJT differential pair – Small signal analysis and CMRR.

UNIT III SINGLE STAGE FET, MOSFET AMPLIFIERS 9

Small Signal Hybrid π equivalent circuit of FET and MOSFET - Analysis of CS, CD and CG amplifiers using Hybrid π equivalent circuits - Basic FET differential pair- BiCMOS circuits.

UNIT IV FREQUENCY RESPONSE OF AMPLIFIERS 9

Amplifier frequency response – Frequency response of transistor amplifiers with circuit capacitors – BJT frequency response – short circuit current gain - cut off frequency – f_{α} , f_{β} and unity gain bandwidth – Miller effect - frequency response of FET - High frequency analysis of CE and MOSFET CS amplifier - Transistor Switching Times.

UNIT V POWER SUPPLIES AND ELECTRONIC DEVICE TESTING 9

Linear mode power supply - Rectifiers - Filters - Half-Wave Rectifier Power Supply - Full-Wave Rectifier Power Supply - Voltage regulators: Voltage regulation - Linear series, shunt and switching Voltage Regulators - Over voltage protection - BJT and MOSFET – Switched mode power supply (SMPS) - Power Supply Performance and Testing - Troubleshooting and Fault Analysis, Design of Regulated DC Power Supply.

TOTAL: 45 PERIODS**OUTCOMES:**

After studying this course, the student should be able to:

- Acquire knowledge of Frequency response characteristics of BJT and FET amplifiers
- Analyze the performance of small signal BJT and FET amplifiers - single stage and multi stage amplifiers
- Apply the knowledge gained in the design of Electronic circuits


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OBJECTIVES :

- To introduce the basic building blocks of linear integrated circuits
- To learn the linear and non-linear applications of operational amplifiers
- To introduce the theory and applications of analog multipliers and PLL
- To learn the theory of ADC and DAC
- To introduce the concepts of waveform generation and introduce some special function ICs

UNIT I BASICS OF OPERATIONAL AMPLIFIERS

9

Current mirror and current sources, Current sources as active loads, Voltage sources, Voltage References, BJT Differential amplifier with active loads, Basic information about op-amps – Ideal Operational Amplifier - General operational amplifier stages -and internal circuit diagrams of IC 741, DC and AC performance characteristics, slew rate, Open and closed loop configurations – JFET Operational Amplifiers – LF155 and TL082.

UNIT II APPLICATIONS OF OPERATIONAL AMPLIFIERS

9

Sign Changer, Scale Changer, Phase Shift Circuits, Voltage Follower, V-to-I and I-to-V converters, adder, subtractor, Instrumentation amplifier, Integrator, Differentiator, Logarithmic amplifier, Antilogarithmic amplifier, Comparators, Schmitt trigger, Precision rectifier, peak detector, clipper and clamper, Low-pass, high-pass and band-pass Butterworth filters.

UNIT III ANALOG MULTIPLIER AND PLL

9

Analog Multiplier using Emitter Coupled Transistor Pair - Gilbert Multiplier cell – Variable transconductance technique, analog multiplier ICs and their applications, Operation of the basic PLL, Closed loop analysis, Voltage controlled oscillator, Monolithic PLL IC 565, application of PLL for AM detection, FM detection, FSK modulation and demodulation and Frequency synthesizing and clock synchronisation.

UNIT IV ANALOG TO DIGITAL AND DIGITAL TO ANALOG CONVERTERS

9

Analog and Digital Data Conversions, D/A converter – specifications - weighted resistor type, R-2R Ladder type, Voltage Mode and Current-Mode R - 2R Ladder types - switches for D/A converters, high speed sample-and-hold circuits, A/D Converters – specifications - Flash type - Successive Approximation type - Single Slope type – Dual Slope type - A/D Converter using Voltage-to-Time Conversion - Over-sampling A/D Converters, Sigma – Delta converters.

UNIT V WAVEFORM GENERATORS AND SPECIAL FUNCTION ICs

9

Sine-wave generators, Multivibrators and Triangular wave generator, Saw-tooth wave generator, ICL8038 function generator, Timer IC 555, IC Voltage regulators – Three terminal fixed and adjustable voltage regulators - IC 723 general purpose regulator - Monolithic switching regulator, Low Drop – Out(LDO) Regulators - Switched capacitor filter .IC MF10, Frequency to Voltage and Voltage to Frequency converters, Audio Power amplifier, Video Amplifier, Isolation Amplifier, Opto-couplers and fibre optic IC.

TOTAL:45 PERIODS**OUTCOMES:**

Upon completion of the course, the student should be able to:

- Design linear and non linear applications of OP – AMPS
- Design applications using analog multiplier and PLL
- Design ADC and DAC using OP – AMPS
- Generate waveforms using OP – AMP Circuits
- Analyze special function ICs



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OBJECTIVES:

- To understand the Architecture of 8086 microprocessor.
- To learn the design aspects of I/O and Memory Interfacing circuits.
- To interface microprocessors with supporting chips.
- To study the Architecture of 8051 microcontroller.
- To design a microcontroller based system

UNIT I THE 8086 MICROPROCESSOR 9

Introduction to 8086 – Microprocessor architecture – Addressing modes - Instruction set and assembler directives – Assembly language programming – Modular Programming - Linking and Relocation - Stacks - Procedures – Macros – Interrupts and interrupt service routines – Byte and String Manipulation.

UNIT II 8086 SYSTEM BUS STRUCTURE 9

8086 signals – Basic configurations – System bus timing –System design using 8086 – I/O programming – Introduction to Multiprogramming – System Bus Structure – Multiprocessor configurations – Coprocessor, Closely coupled and loosely Coupled configurations – Introduction to advanced processors.

UNIT III I/O INTERFACING 9

Memory Interfacing and I/O interfacing - Parallel communication interface – Serial communication interface – D/A and A/D Interface - Timer – Keyboard /display controller – Interrupt controller – DMA controller – Programming and applications Case studies: Traffic Light control, LED display , LCD display, Keyboard display interface and Alarm Controller.

UNIT IV MICROCONTROLLER 9

Architecture of 8051 – Special Function Registers(SFRs) - I/O Pins Ports and Circuits - Instruction set - Addressing modes - Assembly language programming.

UNIT V INTERFACING MICROCONTROLLER 9

Programming 8051 Timers - Serial Port Programming - Interrupts Programming – LCD & Keyboard Interfacing - ADC, DAC & Sensor Interfacing - External Memory Interface- Stepper Motor and Waveform generation - Comparison of Microprocessor, Microcontroller, PIC and ARM processors.

TOTAL: 45 PERIODS**OUTCOMES:**

At the end of the course, the students should be able to:

- Understand and execute programs based on 8086 microprocessor.
- Design Memory Interfacing circuits.
- Design and interface I/O circuits.
- Design and implement 8051 microcontroller based systems.

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EC8004

WIRELESS NETWORKS

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OBJECTIVES:

- To understand the concept about Wireless networks, protocol stack and standards
- To understand and analyse the network layer solutions for Wireless networks
- To study about fundamentals of 3G Services, its protocols and applications
- To have in depth knowledge on internetworking of WLAN and WWAN
- To learn about evolution of 4G Networks, its architecture and applications

UNIT I WIRELESS LAN 9

Introduction-WLAN technologies: - IEEE802.11: System architecture, protocol architecture, 802.11b, 802.11a – Hiper LAN: WATM, BRAN, HiperLAN2 – Bluetooth: Architecture, WPAN – IEEE 802.15.4, Wireless USB, Zigbee, 6LoWPAN, WirelessHART

UNIT II MOBILE NETWORK LAYER 9

Introduction - Mobile IP: IP packet delivery, Agent discovery, tunneling and encapsulation, IPV6- Network layer in the internet- Mobile IP session initiation protocol - mobile ad-hoc network: Routing: Destination Sequence distance vector, IoT: CoAP

UNIT III 3G OVERVIEW 9

Overview of UTMS Terrestrial Radio access network-UMTS Core network Architecture: 3GPP Architecture, User equipment, CDMA2000 overview- Radio and Network components, Network structure, Radio Network, TD-CDMA, TD – SCDMA.

UNIT IV INTERNETWORKING BETWEEN WLANS AND WWANS 9

Internetworking objectives and requirements, Schemes to connect WLANS and 3G Networks, Session Mobility, Internetworking Architecture for WLAN and GPRS, System Description, Local Multipoint Distribution Service, Multichannel Multipoint Distribution System.

UNIT V 4G & Beyond 9

Introduction – 4G vision – 4G features and challenges - Applications of 4G – 4G Technologies: Multicarrier Modulation, Smart antenna techniques, IMS Architecture, LTE, Advanced Broadband Wireless Access and Services, MVNO.

TOTAL:45 PERIODS

OUTCOMES:

Upon completion of the course, the student would be able to:

- Conversant with the latest 3G/4G networks and its architecture
- Design and implement wireless network environment for any application using latest wireless protocols and standards
- Ability to select the suitable network depending on the availability and requirement
- Implement different type of applications for smart phones and mobile devices with latest network strategies



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EC8791

EMBEDDED AND REAL TIME SYSTEMS

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OBJECTIVES:

The student should be made to:

- Understand the concepts of embedded system design and analysis
- Learn the architecture and programming of ARM processor
- Be exposed to the basic concepts of embedded programming
- Learn the real time operating systems

UNIT I INTRODUCTION TO EMBEDDED SYSTEM DESIGN 9

Complex systems and micro processors– Embedded system design process –Design example: Model train controller- Design methodologies- Design flows - Requirement Analysis – Specifications-System analysis and architecture design – Quality Assurance techniques - Designing with computing platforms – consumer electronics architecture – platform-level performance analysis.

UNIT II ARM PROCESSOR AND PERIPHERALS 9

ARM Architecture Versions – ARM Architecture – Instruction Set – Stacks and Subroutines – Features of the LPC 214X Family – Peripherals – The Timer Unit – Pulse Width Modulation Unit – UART – Block Diagram of ARM9 and ARM Cortex M3 MCU.

UNIT III EMBEDDED PROGRAMMING 9

Components for embedded programs- Models of programs- Assembly, linking and loading – compilation techniques- Program level performance analysis – Software performance optimization – Program level energy and power analysis and optimization – Analysis and optimization of program size- Program validation and testing.

UNIT IV REAL TIME SYSTEMS 9

Structure of a Real Time System — Estimating program run times – Task Assignment and Scheduling – Fault Tolerance Techniques – Reliability, Evaluation – Clock Synchronisation.

UNIT V PROCESSES AND OPERATING SYSTEMS 9

Introduction – Multiple tasks and multiple processes – Multirate systems- Preemptive real-time operating systems- Priority based scheduling- Interprocess communication mechanisms – Evaluating operating system performance- power optimization strategies for processes – Example Real time operating systems-POSIX-Windows CE. - Distributed embedded systems – MPSoCs and shared memory multiprocessors. – Design Example - Audio player, Engine control unit – Video accelerator.

TOTAL: 45 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Describe the architecture and programming of ARM processor
- Outline the concepts of embedded systems
- Explain the basic concepts of real time operating system design
- Model real-time applications using embedded-system concepts



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EC8094

SATELLITE COMMUNICATION

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OBJECTIVES:

The student should be made to:

- Understand the basics of satellite orbits
- Understand the satellite segment and earth segment
- Analyze the various methods of satellite access
- Understand the applications of satellites
- Understand the basics of satellite Networks

UNIT I SATELLITE ORBITS

9

Kepler's Laws, Newton's law, orbital parameters, orbital perturbations, station keeping, geo stationary and non Geo-stationary orbits – Look Angle Determination-Limits of visibility – eclipse-Sub satellite point –Sun transit outage-Launching Procedures - launch vehicles and propulsion.

UNIT II SPACE SEGMENT

9

Spacecraft Technology- Structure, Primary power, Attitude and Orbit control, Thermal control and Propulsion, communication Payload and supporting subsystems, Telemetry, Tracking and command-Transponders-The Antenna Subsystem.

UNIT III SATELLITE LINK DESIGN

9

Basic link analysis, Interference analysis, Rain induced attenuation and interference, Ionospheric characteristics, Link Design with and without frequency reuse.

UNIT IV SATELLITE ACCESS AND CODING METHODS

9

Modulation and Multiplexing: Voice, Data, Video, Analog – digital transmission system, Digital video Broadcast, multiple access: FDMA, TDMA, CDMA, DAMA Assignment Methods, compression – encryption, Coding Schemes.

UNIT V SATELLITE APPLICATIONS

9

INTELSAT Series, INSAT, VSAT, Mobile satellite services: GSM, GPS, INMARSAT, LEO, MEO, Satellite Navigational System. GPS Position Location Principles, Differential GPS, Direct Broadcast satellites (DBS/DTH).

TOTAL:45 PERIODS

OUTCOMES:

At the end of the course, the student would be able to:

- Analyze the satellite orbits
- Analyze the earth segment and space segment
- Analyze the satellite Link design
- Design various satellite applications


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