



2.5.2. Mechanism to deal with examination related grievances is transparent, time-bound and efficient

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J.K.K. MUNIRAJAH COLLEGE OF TECHNOLOGY

T.N. PALAYAM



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CIRCULAR

All the faculty members who are handling classes for third year student members of class committee from third year asked to attend the class committee meeting-II for third year at second year class room at 1.30pm on 07.04.2022.

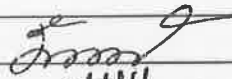
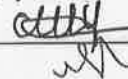
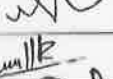


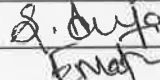
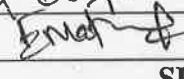
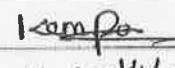
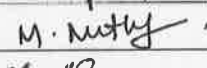
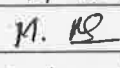
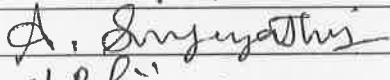

CLASS COMMITTEE MEETING-II			
CLASS: III YEAR ECE	Date:13/04/2022	Time:1.30PM	Venue: II ECE class room
Members: III ECE class handling faculty members & student representatives			
S.NO	AGENDA		
1	Course materials		
2	Syllabus completion-theory courses		
3	Summer project review		
4	Class test II, assignment II, internal test II		
5	Placement activities		
6	General discipline & other activities		


CHAIRPERSON

(Mrs.R.Kokila, AP/ECE)


HOD-ECE

(Dr.C.Murugesan)

NAME OF THE STAFF	DESIGNATION	SIGNATURE
Mr.D.ANANDAKUMAR	AP/ECE	
Ms.N.MYTHILI	AP/ECE	
Mrs.U.SASIKALA	AP/ECE	
Mr.R.PRASANTH	AP/MBA	
Mrs.N.GOMATHI	AP/ECE	
Ms.S.ABINAYA	AP/ECE	
Mr.E.MATHIVANAN	AP/S&H	
NAME OF THE STUDENT	YEAR	SIGNATURE
K.KEMPARAJ	III YEAR	
M.MUTHUPANDI	III YEAR	
M.NARMATHA	III YEAR	
A.SABARI GAYATHRI	III YEAR	
M.ROHINI	III YEAR	



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

MINUTES OF CLASS COMMITTEE MEETING -II

CLASS COMMITTEE MEETING-II		ACADEMIC YEAR:(2021-2022) EVEN	
CLASS:III YEAR ECE	Date: 13/04/2022	Time:: 1.30PM	Venue: II ECE class room
Members::III ECE class handling faculty members & student representatives			
S.NO	AGENDA		
1	Course materials		
2	Syllabus completion-theory courses		
3	Summer project review		
4	Class test II, assignment II, internal test II		
5	Placement activities		
6	General discipline & other activities		

MEMBERS PRESENT

S.NO	MEMBERS	NAME	DESIGNATION/DEPT	SIGNATURE
1	HEAD OF THE DEPARTMENT	Dr.C.MURUGESAN	HOD/ECE	
2	CHAIRPERSON	Mrs.R.KOKILA	AP/ECE	
3	FACULTY MEMBERS HANDLING THE COURSE	Mr.D.ANANDAKUMAR	AP/ECE	
4		Ms.N.MYTHILI	AP/ECE	
5		Mrs.U.SASIKALA	AP/ECE	
6		Mr.R.PRASANTH	AP/MBA	
7		Mrs.N.GOMATHI	AP/ECE	
8		Ms.S.ABINAYA	AP/ECE	
9		Mr.E.MATHIVANAN	AP/S&H	
10		STUDENTS REPRESENTATIVES	K.SATHYA PRIYA	III YEAR ECE



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

The second class committee meeting for third year was conducted at second year ECE class room at 1.30pm.the following points were discussed in the meeting

- The committee members and faculty members were given warm welcome by the head of the department.
- The Chairperson instructed the students to be punctual, regular to the classes and maintain proper dress code.
- Students have been informed about the submission of assignments for all the theory courses.
- Students have been asked to write class tests and assignments.
- Students have been asked to write internal test- II well.
- Students were instructed to meet their respective guides during project hour without fail.
- Re-test will be conducted only for the students who are obtained prior permission (with genuine reason) from the concerned staff members. Strictly Re-test will not be conducted for other students.

FEEDBACK FROM STUDENTS:

S.NO	COURSE CODE	COURSE NAME	NAME OF THE FACULTY	DEPT	SYLLABUS COMPLETION	FEEDBACK FROM STUDENTS	REMEDIAL MEASURES TO BE TAKEN BY THE FACULTY	SIGNATURE OF THE FACULTY
1	EC8691	Microprocessor and Microcontrollers	Mr.D.ANANDA KUMAR	ECE	5 Units Completed	Easy to understand the subject		
2	EC8095	VLSI Design	Ms.N.MYTHILI	ECE	5 Units Completed	Easy to understand the subject		
3	EC8652	Wireless Communication	Mrs.U.SASIKALA	ECE	5 Units Completed	Easy to understand the subject		
4	MG8591	Principles of Management	Mr.R.PRASANTH	MBA	5 Units Completed	Easy to understand the subject		
5	EC8651	Transmission Lines and RF Systems	Mrs.N.GOMATHI	ECE	5 Units Completed	Easy to understand the subject		
6	EC8004	Wireless Networks	Ms.S.ABINAYA	ECE	5 Units Completed	Easy to understand the subject		
7	EC8681	Microprocessor and Microcontrollers Laboratory	Mr.D.ANANDA KUMAR	ECE	10 Exercises Completed	NIL		
8	EC8661	VLSI Design Laboratory	Ms.N.MYTHILI	ECE	10 Exercises Completed	NIL		
9	EC8611	Technical Seminar	Mrs.U.SASIKALA	ECE	5 Presentations Completed	NIL		
10	HS8581	Professional communication	Mr.E.MATHIVANAN	S&H	10 Exercises Completed	NIL		

GRIEVANCES/SUGGESTIONS FROM STUDENTS:

1. Need question bank for the Microprocessor and Microcontrollers
2. Need previous year question papers for the subject of wireless networks



CHAIRPERSON

(Mrs.R.Kokila, AP/ECE)



HOD/ECE

(Dr.C.Murugesan)



PRINCIPAL

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OF TECHNOLOGY
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GOBI (Tk), ERODE (Dt).**



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T.N. PALAYAM



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ACTION TAKEN REPORT- CLASS COMMITTEE MEETING-II

CLASS COMMITTEE MEETING-II		ACADEMIC YEAR:(2021-2022) EVEN	
CLASS:III YEAR ECE	Date: 13/04/2022	Time:1.30pm	Venue: II ECE class room

DATE: 18/04/2022

S.NO	SUGGESTIONS/GRIEVANCES	CORRECTIVE ACTION
1	Need question bank for the Microprocessor and Microcontrollers	Subject incharges are informed to provide question bank of microprocessor and microcontroller
2	Need previous year question papers for the subject of wireless networks	Faculty incharges are informed to give previous year question paper of wireless networks

CHAIRPERSON

(Mrs.R.Kokila, AP/ECE)

HOD/ECE

(Dr.C.Murugesan)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

QUESTION BANK –MICROPROCESSOR AND MICRCONTROLLER

UNIT-I-THE 8086 MICROPROCESSOR

PART-A

1. What is microprocessor?

A microprocessor is a multipurpose, programmable, clock-driven, register-based electronic device that reads binary information from a storage device called memory, accepts binary data as input and processes data according to those instructions, and provides result as output.

2. What is Accumulator?

The Accumulator is an 8-bit register that is part of the arithmetic/logic unit (ALU). This register is used to store 8-bit data and to perform arithmetic and logical operations. The result of an operation is stored in the accumulator. The accumulator is also identified as register A.

3. What is stack?

The stack is a group of memory locations in the R/W memory that is used for temporary storage of binary information during the execution of a program

4. What is a subroutine program?

A subroutine is a group of instructions written separately from the main program to perform a function that occurs repeatedly in the main program. Thus subroutines avoid the repetition of same set of instructions in the main program.

5. Define addressing mode.

Addressing mode is used to specify the way in which the address of the operand is specified within the instruction.

6. Define instruction cycle.

It is defined as the time required to complete the execution of an instruction.

7. Write a program to add a data byte located at offset 0500H in 2000H segment to another data byte

available at 0600H in the same segment and store the result at 0700H in the same segment.

MOV AX, 2000H; initialize DS with value

MOVDS, AX; 2000H

MOV AX, [500H]; Get first data byte from 0500H offset

implemented using the SS and SP registers. It is a LIFO data segment.

12. How is the stack top address calculated?

The stack top address is calculated using the contents of the SS and SP register. The contents of stack segment (SS) register is shifted left by four bit positions (multiplied by 0h) and the resulted 20-bit content is added with the 16-bit offset value of the stack pointer(SP) register.

SS	*	5000H	
SP	*	2050H	
		SS	0101 0000 0000 0000
		10H * SS	0101 0000 0000 0000 0000
		SP	0010 0000 0101 0000
		Stack-top	0101 0010 0000 0101 0000
		Address	5 2 0 5 0

13. What are macros?

Macros are small routines that are used to replace strings in the program. They can have parameters passed to them, which enhances the functionality of the micro itself.

14. How are constants declared?

Constants are declared in the same way as variables, using the format:

Const-Label EQU 012h

15. What is interrupt service routine?

Interrupt means to break the sequence of operation. While the CPU is executing a program an interrupt breaks the normal sequence of execution of instructions & diverts its execution to some other program. This program to which the control is transferred is called the interrupt service routine.

PART-B

- (a) Write an assembly language program in 8086 to search the largest data in the array
(b) Explain the various status flags in 8086.
- (a) Discuss the various addressing modes of 8086.
(b) Explain the following assembler directive in 8086
i. ASSUME ii. EQU iii. DW iv. DD
- (a) Write short notes on Macro
(b) Explain the function of assembler directives.

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W/O	RD	WR	
0	0	0	I/O Read
0	1	0	I/O Write
1	0	0	Memory read
1	1	0	Memory write

5. What is the operation of S0, S1 and S2 pins in maximum mode?

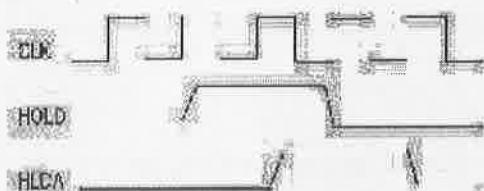
S2, S1, S0 indicates the type of transfer to take place during the current bus cycle.

S ₂	S ₁	S ₀	
0	0	0	Interrupt acknowledge
0	0	1	Read I/O port
0	1	0	Write I/O port
0	1	1	Halt
1	0	0	Instruction fetch
1	0	1	Read Memory
1	1	0	Write Memory
1	1	1	Inactive

6. Give any four pin definitions for maximum mode.

Symbol	Description
QS1, QS0	Reflects the status of the instruction queue. This status indicates the activity in the queue during the previous clock cycle.
LOCK	Indicates that the bus is not to be relinquished to other potential bus masters.
RQ/GT1	For inputting bus requests and outputting bus grants.
RQ/GT0	Same as RQ/GT1 except that a request on RQ/GT0 has higher priority.

7. Draw the bus request and bus grant timings in minimum mode system.



Bus request and bus grant timings in minimum

12. What are pointers and index registers?

IP, BP and SP are the pointers and contain offsets within the code, data and stack segments respectively. SI and DI are the index registers, which are used as general purpose registers and also for offset storage in case of indexed, based indexed and relative based indexed addressing modes

13. How is the physical address calculated? Give an example.

The physical address, which is 20-bits long is calculated using the segment and off set registers, each 16-bits long. The segment address is shifted left bit-wise four times and offset address is added to this to produce a 20 bit physical address.

14. What is meant by memory segmentation?

Memory segmentation is the process of completely dividing the physically available memory into a number of logical segments. Each segment is 64K byte in size and is addressed by one of the segment register.

15. What are the advantages of segmented memory?

The advantages of segmented memory are:

- i. Allows the memory capacity to be 1Mbyte, although the actual addresses to be handled are of 16-bit size.
- ii. Allows the placing of code, data and stack portions of the same program in different parts of memory for data and code protection.
- iii. Permits a program and/or its data to be put into different areas of memory, each times program is executed i.e., provision for relocation may be done.

PART-B

1. (a) Draw and explain the maximum mode of 8086
(b) List the advantages of multiprocessor system
2. (i) Show the pin configuration and function of signals of 8086 microprocessor.
(ii) Show the memory organization and interfacing with 8086 microprocessor. Explain how the memory is accessed.
3. (a) Explain the functions of
 - i. HLDA
 - ii. RQ/GT0
 - iii. DEN
 - iv. ALE
(b) Draw and explain the minimum mode of 8086
4. (a) Draw and explain the block diagram of minimum mode of operation
(b) Write notes on addressing memory



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represents bit set reset mode operation. The BSR mode is used to set or reset the bits in port C.

9. What is mode 0 operation of 8255?

In this mode, ports A and B are used as two simple 8-bit I/O ports and port C as two 4-bit ports. Each port can be programmed to function as an input port or an output port. The input/ output features in mode 0 as follows:

- i. Outputs are latched
- ii. Inputs are not latched
- iii. Ports do not have handshake or interrupt capability.

10. What are the modes of operation supported by 8255?

- i. Bit set reset mode (BSR)
 - ii. I/O mode
- Mode 0
Mode 1
Mode 2

11. What is ADC and DAC?

The electronic circuit that translates an analog signal into a digital signal is called analog-to-digital converter(ADC).The electronic circuit translates a digital signal into an analog signal is called Digital-to-analog Converter(DAC).

12. Define conversion time.

It is defined as the total time required to convert an analog signal into a digital output. It is determined the conversion technique used and by the propagation delay in various circuits.

13. What is resolution time in ADC?

It is defined as a ratio of change in value of input voltage V_i , needed to change the digital output by 1 LSB. If the full scale input voltage required to cause a digital output of all 1's is V_{iFS} . Then the resolution can be given as

$$\text{Resolution} = V_{iFS} / (2^n - 1)$$

14. List the functions performed by 8279.

- i. It has built-in hardware to provide key debounce.
- ii. It provides a scanned interface to a 64 contact key matrix.
- iii. It provides multiplexed display interface with blanking and inhibit options.

4. What is stack pointer (sp)

Stack pointer (SP) is a 8 bit wide register and is incremented before the data is stored into the stack using PUSH or CALL instructions. It contains 8-bit stack top address. It is defined anywhere in the on-chip 128-byte RAM. After reset, the SP register is initialized to 07. After each write to stack operation, the 8-bit contents of the operand are stored onto the stack, after incrementing the SP register by one. It is not a top-down data structure. It is allotted an address in the special function register bank.

5. What is data pointer (DTPR)?

It is a 16-bit register that contains a higher byte (DPH) and lower byte (DPL) of a 16-bit external data RAM address. It is accessed as a 16-bit register or two 8-bit registers. It has been allotted two addresses in the special function register bank, for its two bytes DPH and DPL.

6. Why oscillator circuit is used?

Oscillator circuit is used to generate the basic timing clock signal for the operation of the circuit using crystal oscillator.

7. What is the purpose of using instruction register?

Instruction register is used for the purpose of decoding the opcode of an instruction to be executed and gives information to the timing and control unit generating necessary signals for the execution of the instruction.

8. Give the purpose of ALE/PROG signal

ALE/PROG is an address latch enable output pulse and indicates that valid address bits available on the respective pins. The ALE pulses are emitted at a rate of one-sixth of the oscillator frequency. The signal is valid only for external memory accesses.

9. What are addressing modes?

The various ways of accessing data are called addressing modes.

10. Give the addressing modes of 8051?

There are six addressing modes in 8051. They are

- Direct addressing
- Indirect addressing
- Register instruction
- Register specific (register implicit)
- Immediate mode
- Indexed addressing

11. What is direct addressing mode?

The operands are specified using the 8-bit address field, in the instruction format. Only internal data Ram and SFRS can be directly addressed. This is known as direct addressing mode.

It may be used for external timing or clockwise purpose. One ALE pulse is skipped during

PART-B

1. With the necessary diagram of control word format, explain the various operating modes of timer in 8051 microcontroller
2. With the help of neat diagram explain the memory organization of 8051 microcontroller
3. With neat sketch explain the architecture/ functional block diagram of 8051 microcontroller.
4. Draw the Pin Diagram of 8051 and explain the function of various signals.
5. List the various Instructions available in 8051 microcontroller and explain.

UNIT-V

INTERFACING MICROCONTROLLERS

PART-A

1. What is a serial data buffer?

Serial data buffer is a special function register and it initiates serial transmission when byte is written to it and if read, it reads received serial data. It contains two independent registers internally. One of them is a transmit buffer, which is a parallel-in serial-out register. The other is a receive buffer, which is a serial-in parallel-out register

2. What are timer registers?

Timer registers are two 16-bit registers and can be accessed as their lower and upper bytes. TLO represents the lower byte of the timing register 0, while TH0 represents higher bytes of the timing register 0. Similarly, T1L and TH1 represent lower and higher bytes of timing register 1. These registers can be accessed using the addresses allotted to them, which lie in the special function registers address range, i.e., 80H to FF

3. What is the use of timing and control unit?

Timing and control unit is used to derive all the necessary timing and control signals required for the internal operation of the circuit. It also derives control signals that are required for controlling the external system bus.

4. When are timer overflow bits set and reset?

The timer overflow bits are set when timer rolls over and reset either by the execution of an RET instruction or by software, manually clearing the bits. The bits are located in the TCON register along with timer run control (TRn) bits.

5. Explain the mode (0 and 1) operation of the timer.

The operations are as follows

- Timer mode 0 and 1 operations are similar for the 13 bit (mode 0) or 16 bit (mode 1) counter. When the timer reaches the limits of the count, the overflow flag is set and the counter is reset back to zero. The modes 0 and 1 can be used to time external events.

- ET1: Enable or disable the timer 1 overflow interrupt.
- EX1: Enable or disable external interrupt 1.
- ET0: Enable or disable the timer 0 overflow interrupt.
- EX0: Enable or disable external interrupt 0.

10. What is meant by nesting of interrupts?

Nesting of interrupts means that interrupts are re-enabled inside an interrupt handler. If another interrupt request comes in, while the first interrupt handler is executing, processor execution will acknowledge the new interrupt and jump to its vector.

11. How is the 8051 serial port different from other micro controllers?

The 8051 serial port is a very complex peripheral and able to send data synchronously and asynchronously in a variety of different transmission modes.

12. Explain synchronous data transmission.

- In synchronous mode (mode 0), the instruction clock is used.
- Data transfer is initiated by writing to the serial data port address.
- Txd pin is used for clock output, while Rxd pin is for data transfer.
- When a character is received, the status of the data transfer is monitored by polling the RI-n bit in serial control register (SCON).

13. Give an application for synchronous serial communication.

An application for synchronous serial communication is RS-232.

14. When is an external memory access generated in 8051?

In 8051, during execution the data is fetched continuously. Most of the data is executed out of the 8051's built-in control store. When an address is outside the internal control store, an external memory access is generated.

15. Give the priority level of the interrupt source

Interrupt source Priority within a level

IE0 (External INT0)

TF0 (Timer 0)

IE 1 (External INT 1)

TF 1 (Timer 1)

RI = TI (Serial port) Highest



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

PREVIOUS YEAR QUESTION PAPERS OF WIRELESS NETWORKS

12/04/19
FR

Reg. No. :



Question Paper Code : 52930

B.E. (Part - Time) Tech. DEGREE EXAMINATIONS, APRIL/MAY 2019.

Eighth Semester

Electronics and Communication Engineering

EC 6802 — WIRELESS NETWORKS

(Regulation 2013)

(Common to : PTEC 6802 – Wireless Networks for B.E. (Part – Time) – Seventh Semester – Electronics and Communication Engineering (Regulations – 2014))

Time : Three hours

Maximum : 100 marks

Answer ALL questions.

PART A — (10 × 2 = 20 marks)

1. Identify the need of WATM systems.
2. What are Piconet and Scatternet?
3. Define the term Care-of address in mobile IP.
4. Outline the characteristics of MANET.
5. Give any four schemes to improve the TCPs performance in wireless networks.
6. Identify the characteristics to be considered while deploying applications over 3G wireless links.
7. What is UMTS?
8. Define the Long-Term Evolution (LTE).
9. List the features of 4G.
10. Identify the focuses of Cognitive Radio.



Reg. No. :

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Question Paper Code : 50457

B.E./B.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2017

Eighth Semester

Electronics and Communication Engineering

EC6802 – WIRELESS NETWORKS

(Regulations 2013)

Time : Three Hours

Maximum : 100 Marks

Answer ALL questions

PART - A (10×2=20 Marks)

1. State the MAC management functions.
2. What is the functionality L2CAP ? List the different types of logical channels.
3. When the agent solicitation message has to be sent by mobile node ?
4. Why is routing in multi-hop ad-hoc networks complicated ?
5. What is the need for I-TCP ?
6. Define fast recovery.
7. How is isolation between users in the downlink accomplished in a WCDMA system ?
8. What is meant by firewall ?
9. List some of the applications of 4G system.
10. What is cognitive radio ?

PART - B

(5×16=80 Marks)

11. a) Explain in detail about the IEEE 802.11 protocol architecture and bridging with other networks.

(OR)

- b) Define HiperLan-2. Discuss about the various operation modes and protocol stack in HiperLan-2.

01/11/18 (FN)

Reg. No. :

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Question Paper Code : 20431

B.E./B.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2018.

Eighth Semester

Electronics and Communication Engineering

EC 6802 — WIRELESS NETWORKS

(Regulations 2013)

(Also common to PTEC 6802 – Wireless Networks for B.E. Part-Time
Seventh Semester – Electronics and Communication Engineering
Regulations 2014)

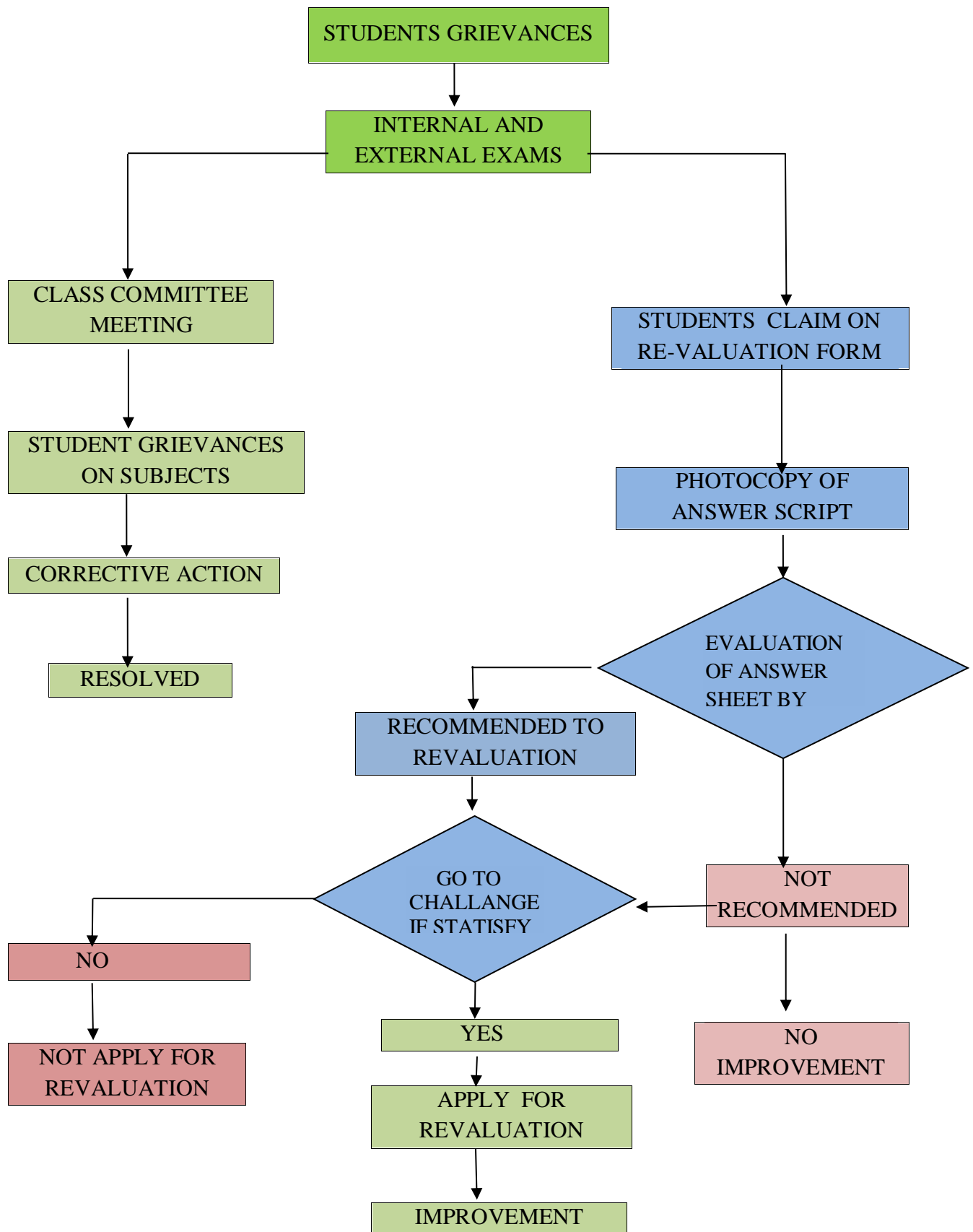
Time : Three hours

Maximum : 100 marks

Answer ALL questions.

PART A — (10 × 2 = 20 marks)

1. What is the principle behind infrared technology? What are the advantages and disadvantages of infrared technology?
2. What is WIMAX? Mention its features.
3. Define SIP. Write the functions of SIP.
4. Differentiate proactive and reactive routing protocols. Write examples for each.
5. What is I-TCP? List its merits and demerits.
6. What is Congestion Avoidance algorithm?
7. Name the functions of Radio Network Control (RNC).
8. List the functions provided by 3G-GGSN.
9. What is meant by Multi Carrier Modulation (MCM)? Mention its merits and demerits.
10. What are the techniques to improve network survivability in different layers? Name the challenges faced by 4G.





2.5.2 Mechanism to deal with examination related grievances

Student Grievances related to University examination re-evaluation process

- After examination, The University evaluates the answer sheets in a centralized manner.
- The results of the examination are published in the university website.
- If a student is not satisfied with his/her results, he/she can apply for a photocopy of his/her answer sheet.
- Then, the student can apply for revaluation of his/her answer sheets after getting approval from the head of the department.
- In case, if a student is not satisfied with the outcome of the revaluation or the marks awarded in the revaluation, he/she can go for a challenge

University semester exam re-evaluation data for the academic year 2021-2022 even semester

S.NO	Programme Name	No. of students answer script applied	No. of subjects answer script applied	No. of subjects Revaluation Applied	No. of subjects mark changed
1	B.E. Automobile Engineering	-	-	-	-
2	B.E. Civil Engineering	-	-	-	-
3	B.E. Computer Science and Engineering	-	-	-	-
4	B.E. Electrical and Electronics Engineering	03	03	0	0
5	B.E. Electronics and Communication Engineering	-	-	-	-
6	B.E. Mechanical Engineering	-	-	-	-
7	B.Tech. Information Technology	-	-	-	-
8	M.E. Applied Electronics	-	-	-	-
9	M.E. Computer Science and Engineering	-	-	-	-
10	M.E. Manufacturing Engineering	-	-	-	-
11	M.E. Power Electronics and Drives	-	-	-	-
12	MCA. Master of computer applications	-	-	-	-
13	PG-MBA. Masters of Business Administration	-	-	-	-



OFFICE OF THE CONTROLLER OF EXAMINATIONS
ANNA UNIVERSITY :: CHENNAI – 25
PROCEDURE FOR OBTAINING PHOTOCOPY OF ANSWER SCRIPTS
NOVEMBER / DECEMBER 2019 EXAMINATIONS

1. The Colleges were provided the softcopy of the results of November / December 2019 examinations in pdf format through the Zonal Offices. Based on that the students who are not satisfied with the results may apply for the photocopy of their answer scripts to apply for revaluation.
2. *Candidates who wish to apply for revaluation should first apply for photocopy of his/her answer script by paying Rs.300/- per script on or before 04-02-2020. The Principals are requested to register for the same in the web portal on or before 05-02-2020. The web portal will be closed on 05-02-2020 at 5.00PM.*
3. After receiving the photocopy, the student can verify the answer script for any discrepancy like total mistake and omissions in the valuation and the same may be brought to the notice of the Controller of Examinations for remedial action..
4. Discrepancies such as missing of pages, answer scripts not belonging to the student etc. may be reported through the web-portal. After the problem is solved i.e. receipt of the copy of the correct answer script, it may be updated in the web-portal as "PROBLEM SOLVED". Only after solving the issue, the revaluation of the answer scripts will be permitted.
5. The students of closed colleges may apply for photocopy manually through the Zonal Offices concerned. However, the students of closed colleges within the Zones 1 to 4 may apply for photocopy through the office of the Controller of Examinations, Anna University, Chennai.
6. The valuation in the photocopy of the answer script can be verified by the subject expert and if the expert is convinced that the script deserves higher marks than awarded, he/she can recommend for applying revaluation.
7. The application for revaluation of answer scripts for the persons obtained photocopy will be intimated after the supply of photocopy.
8. Candidates who have applied for Photocopy and Revaluation alone are eligible for the Review for their answer script (by remitting the prescribed fee) after the Publication of the Revaluation Results. The details of the Review Procedure will be announced along with the revaluation results.

CONTROLLER OF EXAMINATIONS



J.K.K. MUNIRAJAH COLLEGE OF TECHNOLOGY

Approved by AICTE, New Delhi and Affiliated to Anna University, Chennai.

T.N. Palayam (Po), Gobi (Tk), Erode (Dt) – 638 506



Evidence of apply to photocopy of answer sheet to university for the even semester 2021-2022

Register No.	Name of the Student	No. of Subjects	Fees in Rs.
731218105013	MEHAALYENNI C	1	300.00
731218105014	NAVEENMARSHAL Y	1	300.00
731218105302	GOKULNATH A	1	300.00
No. of Students :3		No. of Subjects :3	Total : 900.00
Total No. of Students: 3		Total No. of Subjects : 3	Grand Total : 900.00

ANNA UNIVERSITY :: CHENNAI - 600 025
OFFICE OF THE CONTROLLER OF EXAMINATIONS
Abstract of Photocopies required & Fees Collected
APRIL / MAY EXAMINATION,2022- EXAMINATIONS

PC

Inst Code & Name : 7312 - J K K MUNIRAJAH COLLEGE OF TECHNOLOGY

Branch Code / Name : 105 : B.E. Electrical and Electronics Engineering University : AUC

The Followings are Enclosed.

i.Obtaining Photocopies of answer book applications received from the students: 3
NEFT NO: SBIN322285137954

ii. DD No. _____ dated 12-10-2022 drawn on (Bank)
SBI BANK, T.N.PALAYAM for Rs.900.

College Seal

Encl : As above.

To
The Controller of Examinations
Anna University , Chennai - 600 025.

Signature of Principal
PRINCIPAL
Name: MUNIRAJAH COLLEGE OF TECHNOLOGY
T.N. PALAYAM (Po)-638 506.
Date: GOBI (TK), ERODE (Dt).



J.K.K. MUNIRAJAH COLLEGE OF TECHNOLOGY

Approved by AICTE, New Delhi and Affiliated to Anna University, Chennai.

T.N. Palayam (Po), Gobi (Tk), Erode (Dt) – 638 506



Evidence of recommended form to apply re-evaluation of answer sheet by head of the department for the even semester 2021-22



ANNA UNIVERSITY
CHENNAI - 600 025
APPLICATION FOR PHOTOCOPY
APRIL / MAY EXAMINATION, 2022

PC

INSTRUCTION TO CANDIDATES

1. Fee for obtaining Photocopy is Rs.300/- per answer script and shall be paid at the College only.
2. Application for Photocopy of answer scripts must be submitted to the Principal of the concerned College on or before 29-08-2022
3. The candidate is advised to register for the subject, in which Photocopy is sought for within the stipulated time for the next examination without waiting for the supply of Photocopy/results of Revaluation.
4. There is no provision for Photocopy of Practical/Project examination Papers.
5. Incomplete/defective application will be rejected and the fee will neither be refunded nor adjusted towards any fee due to the University.
6. No application will be accepted beyond the due date prescribed.
7. The Head of the Department should ensure while recommending application that the subject code and the subject(s) filled in the respective columns by the candidate are verified and found to be correct.

1. Name	MEHAALYENNI C
2. Register Number	731218105013
3. College Code / Name	7312 - J K K MUNIRAJAH COLLEGE OF TECHNOLOGY
4. Degree & Branch	B.E. Electrical and Electronics Engineering
5. Month & Year of Examination	MAY 2022
6. No. of Subjects applied for Photocopy	1
7. Amount of fee paid to the College	Rs. 300

8. Subjects for which photocopies of valued answers scripts required:

Semester No.	Subject Code	Subject Title	Grade	Result
08	EE8015	Electric energy generation, utilization and conservation	U	RA

9. Recommendations of the HOD

Recommended.

Signature of the HOD

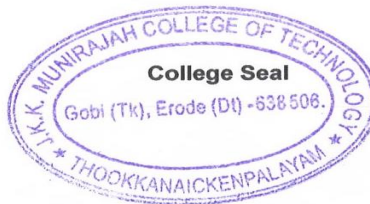
C. Gopal Das
27/08/22

Signature of the Candidate

C. M. Chalap

Station : T. N. Palayam

Date : 27/08/2022.



Prasad
Signature of the Principal
PRINCIPAL
JKK MUNIRAJAH COLLEGE
OF TECHNOLOGY
T.N. PALAYAM (Po)-638 506.
GOBI (TK), ERODE (Dt).



J.K.K. MUNIRAJAH COLLEGE OF TECHNOLOGY

Approved by AICTE, New Delhi and Affiliated to Anna University, Chennai.

T.N. Palayam (Po), Gobi (Tk), Erode (Dt) – 638 506



University semester exam re-evaluation data for the academic year 2021-2022 even semester

S.No	Reg.no	Students Name	Subject code	Year/sem	Apply answer script	Apply revaluation	If mark changed
B.E. Electrical and Electronics Engineering							
1	731218105013	MEHAALYENNI C	EE8015	IV/08	✓	-	-
2	731218102005	NAVEENMARSHAL Y	EE8017	IV/08	✓	-	-
3	731218105302	GOKULNATH A	EE8017	IV/08	✓	-	-



Evidence of answer sheet re-evaluated by subject specialist

731218105302

Sub:EE8017

Mark:20 Coll: 7312

Zone S No.: 1018

1018

1018

(To be filled in by the candidate)

Date: 2.7.22 Session: F.N

Subject Code / Title: EE8017/ High voltage direct current transmi

Question Paper Code: 20501 No. of Pages used: 27

(To be filled in by the candidate)

Date: 2.7.22 Session: F.N Question Paper Code: 20501

Subject Code / Title: EE8017 High voltage direct current transmi

Instruction to the Candidate: Put a tick mark (✓) for the questions attended in the tick mark column against each question in V-1, V-2 & V-3

PART - A			PART - B & C						Total Marks	Grand Total (in words)
Question No.	✓	Marks	Question No.	i	ii	iii	iii	iii		
				✓	Marks	✓	Marks	✓	Marks	
1		✓	11	✓	2	✓	2		4	700 2500 1018
2		✓		b						
3		✓	12	✓	4				4	
4	✓	0		b						
5	✓	0	13	✓	2	✓	2		4	
6		✓		a						
7	✓	0	14	✓	4				4	
8	✓	0		b						
9	✓	0	15	✓	2				2	
10	✓	0		a						
			16	✓	2				2	
				b						
Total		00							20	20

Declaration by the Examiner: Verified that all the questions attended by the student are valued and the total is found to be correct



Student Grievances related to University examination re-evaluation process

- After examination, The University evaluates the answer sheets in a centralized manner.
- The results of the examination are published in the university website.
- If a student is not satisfied with his/her results, he/she can apply for a photocopy of his/her answer sheet.
- Then, the student can apply for revaluation of his/her answer sheets after getting approval from the head of the department.
- In case, if a student is not satisfied with the outcome of the revaluation or the marks awarded in the revaluation, he/she can go for a challenge

University semester exam re-evaluation data for the academic year 2021-2022 ODD semester

S.NO	Programme Name	No. of students answer script applied	No. of subjects answer script applied	No. of subjects Revaluation Applied	No. of subjects mark changed
1	B.E. Automobile Engineering	-	-	-	-
2	B.E. Civil Engineering	-	-	-	-
3	B.E. Computer Science and Engineering	01	01	0	0
4	B.E. Electrical and Electronics Engineering	02	04	0	0
5	B.E. Electronics and Communication Engineering	02	04	0	0
6	B.E. Mechanical Engineering	-	-	-	-
7	B.Tech. Information Technology	01	01	0	0
8	M.E. Applied Electronics	-	-	-	-
9	M.E. Computer Science and Engineering	-	-	-	-
10	M.E. Manufacturing Engineering	-	-	-	-
11	M.E. Power Electronics and Drives	-	-	-	-
12	MCA. Master of computer applications	-	-	-	-
13	PG-MBA. Masters of Business Administration	-	-	-	-



J.K.K. MUNIRAJAH COLLEGE OF TECHNOLOGY

Approved by AICTE, New Delhi and Affiliated to Anna University, Chennai.

T.N. Palayam (Po), Gobi (Tk), Erode (Dt) – 638 506



Evidence of apply to photocopy of answer sheet to university for the academic year 2021-2022 odd semester

ANNA UNIVERSITY :: CHENNAI - 600 025		OFFICE OF THE CONTROLLER OF EXAMINATIONS		PC
Abstract of Photocopies required & Fees Collected				
NOV. / DEC. EXAMINATION, 2021- EXAMINATIONS				
Inst Code & Name : 7312 - J K K MUNIRAJAH COLLEGE OF TECHNOLOGY				
Branch Code / Name : 104 : B.E. Computer Science and Engineering		University : AUC		
Register No.	Name of the Student	No. of Subjects	Fees in Rs.	
731221104007	BAMBAVASAN V	1	300.00	
No. of Students :1		No. of Subjects :1		Total : 300.00
Branch Code / Name : 105 : B.E. Electrical and Electronics Engineering		University : AUC		
Register No.	Name of the Student	No. of Subjects	Fees in Rs.	
731221105002	DEVI M	3	900.00	
731221105009	ROJA S	1	300.00	
No. of Students :2		No. of Subjects :4		Total : 1200.00
Branch Code / Name : 106 : B.E. Electronics and Communication Engineering		University : AUC		
Register No.	Name of the Student	No. of Subjects	Fees in Rs.	
731221106004	KAVIYA K	2	600.00	
731221106010	SANTHIYA Y	2	600.00	
No. of Students :2		No. of Subjects :4		Total : 1200.00
Branch Code / Name : 205 : B.Tech. Information Technology		University : AUC		
Register No.	Name of the Student	No. of Subjects	Fees in Rs.	
731221205042	TAMILSELVAN M	1	300.00	
No. of Students :1		No. of Subjects :1		Total : 300.00
Total No. of Students: 6		Total No. of Subjects : 10		Grand Total : 3000.00
The Followings are Enclosed.				
i. Obtaining Photocopies of answer book applications received from the students: 6				
ii. <u>JR No: SBIN122196424602</u> dated <u>15/01/2022</u> drawn on (Bank)				
<u>STATE BANK OF INDIA T.N. PALAYAM</u> for Rs.3000.				




J.K.K. MUNIRAJAH COLLEGE OF TECHNOLOGY

Approved by AICTE, New Delhi and Affiliated to Anna University, Chennai.

T.N. Palayam (Po), Gobi (Tk), Erode (Dt) – 638 506



 ANNA UNIVERSITY :: CHENNAI - 600 025
OFFICE OF THE CONTROLLER OF EXAMINATIONS
Abstract of Photocopies required & Fees Collected
NOV. / DEC. EXAMINATION, 2021- EXAMINATIONS

PC

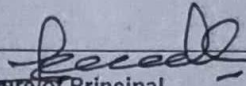
Inst Code & Name : 7312 - J K K MUNIRAJAH COLLEGE OF TECHNOLOGY

College Seal

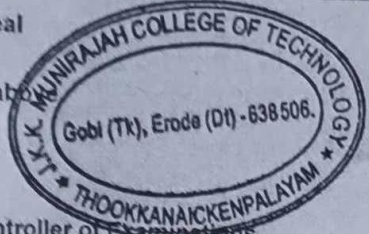
Encl : As above

To

The Controller of Examinations
Anna University , Chennai - 600 025.


Signature of Principal

Name : **Dr.K. SRIDHARAN**
Principal
J.K.K. MUNIRAJAH COLLEGE OF TECHNOLOGY
T.N. PALAYAM (PO), GOBI (TK), ERODE (DT),
TAMILNADU, PIN : 638 506.


MUNIRAJAH COLLEGE OF TECHNOLOGY
Gobi (Tk), Erode (Dt) - 638 506.
THOOKKANAICKENPALAYAM



J.K.K. MUNIRAJAH COLLEGE OF TECHNOLOGY

Approved by AICTE, New Delhi and Affiliated to Anna University, Chennai.

T.N. Palayam (Po), Gobi (Tk), Erode (Dt) – 638 506



Evidence of recommended form to apply re-evaluation of answer sheet by head of the Department for the academic year 2021-2022 odd semester

PC

**ANNA UNIVERSITY
CHENNAI - 600 025
APPLICATION FOR PHOTOCOPY
NOV. / DEC. EXAMINATION, 2021**

INSTRUCTION TO CANDIDATES

1. Fee for obtaining Photocopy is Rs.300/- per answer script and shall be paid at the College only.
2. Application for Photocopy of answer scripts must be submitted to the Principal of the concerned College on or before 07-07-2022
3. The candidate is advised to register for the subject, in which Photocopy is sought for within the stipulated time for the next examination without waiting for the supply of Photocopy/results of Revaluation.
4. There is no provision for Photocopy of Practical/Project examination Papers.
5. Incomplete/defective application will be rejected and the fee will neither be refunded nor adjusted towards any fee due to the University.
6. No application will be accepted beyond the due date prescribed.
7. The Head of the Department should ensure while recommending application that the subject code and the subject(s) filled in the respective columns by the candidate are verified and found to be correct.

1. Name	ROJA S
2. Register Number	731221105009
3. College Code / Name	7312 - J K K MUNIRAJAH COLLEGE OF TECHNOLOGY
4. Degree & Branch	B.E. Electrical and Electronics Engineering
5. Month & Year of Examination	NOV 2021
6. No. of Subjects applied for Photocopy	1
7. Amount of fee paid to the College	Rs. 300

8. Subjects for which photocopies of valued answers scripts required:

Semester No.	Subject Code	Subject Title	Grade	Result
01	HS3151	Professional english - I	U	RA

9. Recommendations of the HOD

Recommended:
M. B. S. S. S.
Signature of the HOD

X S. Raja
Signature of the Candidate

Station : T.N. Palayam

Date : 13/07/22

J.K.K. MUNIRAJAH COLLEGE OF TECHNOLOGY
College Seal
Gobi (Tk), Erode (Dt) - 638 506.
THOOKKANAICKENPALAYAM

Signature of the Principal
Dr.K. SRIDHARAN
Principal
J.K.K. MUNIRAJAH COLLEGE OF TECHNOLOGY
T.N. PALAYAM (PO), GOBI (TK), ERODE (DT).
TAMILNADU, PIN : 638 506.



University semester exam re-evaluation data for the academic year 2021-2022 odd semester

S.No	Reg.no	Students Name	Subject code	Year/sem	Apply answer script	Apply revaluation	If mark changed
B.E. Computer science and Engineering							
1	731221104007	BAMBAVASAN V	GE3151	I/01	✓	-	-
B.E. Electrical and Electronics Engineering							
2	731221105002	DEVI M	HS3151	I/01	✓	-	-
			GE3151	I/01	✓	-	-
			CY3151	I/01	✓	-	-
3	731221105009	ROJA S	HS3151	I/01	✓	-	-
B.E. Electronics and communication Engineering							
4	731221106004	KAVIYA K	MA3151	I/01	✓	-	-
			PH3151	I/01	✓	-	-
5	731221106010	SANTHIYA Y	PH3151	I/01	✓	-	-
			CY3151	I/01	✓	-	-
B.tech. Information Technology							
6	731221205042	TAMILSELVAN M	PH3151	I/01	✓	-	-