

EC8811

PROJECT WORK

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**OBJECTIVES:**

- To develop the ability to solve a specific problem right from its identification and literature review till the successful solution of the same. To train the students in preparing project reports and to face reviews and viva voce examination.

The students in a group of 3 to 4 works on a topic approved by the head of the department under the guidance of a faculty member and prepares a comprehensive project report after completing the work to the satisfaction of the supervisor. The progress of the project is evaluated based on a minimum of three reviews. The review committee may be constituted by the Head of the Department. A project report is required at the end of the semester. The project work is evaluated based on oral presentation and the project report jointly by external and internal examiners constituted by the Head of the Department.

**TOTAL: 300 PERIODS**

**OUTCOME:**

- On Completion of the project work students will be in a position to take up any challenging practical problems and find solution by formulating proper methodology.



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**OBJECTIVES:**

- To acquaint the students with the construction, theory and operation of the basic electronic devices such as PN junction diode, Bipolar and Field effect Transistors, Power control devices, LED, LCD and other Opto-electronic devices

**UNIT I SEMICONDUCTOR DIODE 9**

PN junction diode, Current equations, Energy Band diagram, Diffusion and drift current densities, forward and reverse bias characteristics, Transition and Diffusion Capacitances, Switching Characteristics, Breakdown in PN Junction Diodes.

**UNIT II BIPOLAR JUNCTION TRANSISTORS 9**

NPN -PNP -Operations-Early effect-Current equations – Input and Output characteristics of CE, CB, CC - Hybrid - $\pi$  model - h-parameter model, Ebers Moll Model- Gummel Poon-model, Multi Emitter Transistor.

**UNIT III FIELD EFFECT TRANSISTORS 9**

JFETs – Drain and Transfer characteristics, -Current equations-Pinch off voltage and its significance- MOSFET- Characteristics- Threshold voltage -Channel length modulation, D- MOSFET, E- MOSFET- Characteristics – Comparison of MOSFET with JFET.

**UNIT IV SPECIAL SEMICONDUCTOR DEVICES 9**

Metal-Semiconductor Junction- MESFET, FINFET, PINFET, CNTFET, DUAL GATE MOSFET, Schottky barrier diode-Zener diode-Varactor diode –Tunnel diode- Gallium Arsenide device, LASER diode, LDR.


**UNIT V POWER DEVICES AND DISPLAY DEVICES 9**

UJT, SCR, Diac, Triac, Power BJT- Power MOSFET- DMOS-VMOS. LED, LCD, Photo transistor, Opto Coupler, Solar cell, CCD.

**TOTAL : 45 PERIODS****OUTCOMES:**

**At the end of the course the students will be able to:**

- Explain the V-I characteristic of diode, UJT and SCR
- Describe the equivalence circuits of transistors
- Operate the basic electronic devices such as PN junction diode, Bipolar and Field effect Transistors, Power control devices, LED, LCD and other Opto-electronic devices

  
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**OBJECTIVES :**

- To introduce the basic building blocks of linear integrated circuits
- To learn the linear and non-linear applications of operational amplifiers
- To introduce the theory and applications of analog multipliers and PLL
- To learn the theory of ADC and DAC
- To introduce the concepts of waveform generation and introduce some special function ICs

**UNIT I BASICS OF OPERATIONAL AMPLIFIERS**

9

Current mirror and current sources, Current sources as active loads, Voltage sources, Voltage References, BJT Differential amplifier with active loads, Basic information about op-amps – Ideal Operational Amplifier - General operational amplifier stages -and internal circuit diagrams of IC 741, DC and AC performance characteristics, slew rate, Open and closed loop configurations – JFET Operational Amplifiers – LF155 and TL082.

**UNIT II APPLICATIONS OF OPERATIONAL AMPLIFIERS**

9

Sign Changer, Scale Changer, Phase Shift Circuits, Voltage Follower, V-to-I and I-to-V converters, adder, subtractor, Instrumentation amplifier, Integrator, Differentiator, Logarithmic amplifier, Antilogarithmic amplifier, Comparators, Schmitt trigger, Precision rectifier, peak detector, clipper and clamper, Low-pass, high-pass and band-pass Butterworth filters.

**UNIT III ANALOG MULTIPLIER AND PLL**

9

Analog Multiplier using Emitter Coupled Transistor Pair - Gilbert Multiplier cell – Variable transconductance technique, analog multiplier ICs and their applications, Operation of the basic PLL, Closed loop analysis, Voltage controlled oscillator, Monolithic PLL IC 565, application of PLL for AM detection, FM detection, FSK modulation and demodulation and Frequency synthesizing and clock synchronisation.

**UNIT IV ANALOG TO DIGITAL AND DIGITAL TO ANALOG CONVERTERS**

9

Analog and Digital Data Conversions, D/A converter – specifications - weighted resistor type, R-2R Ladder type, Voltage Mode and Current-Mode R - 2R Ladder types - switches for D/A converters, high speed sample-and-hold circuits, A/D Converters – specifications - Flash type - Successive Approximation type - Single Slope type – Dual Slope type - A/D Converter using Voltage-to-Time Conversion - Over-sampling A/D Converters, Sigma – Delta converters.

**UNIT V WAVEFORM GENERATORS AND SPECIAL FUNCTION ICs**

9

Sine-wave generators, Multivibrators and Triangular wave generator, Saw-tooth wave generator, ICL8038 function generator, Timer IC 555, IC Voltage regulators – Three terminal fixed and adjustable voltage regulators - IC 723 general purpose regulator - Monolithic switching regulator, Low Drop – Out(LDO) Regulators - Switched capacitor filter IC MF10, Frequency to Voltage and Voltage to Frequency converters, Audio Power amplifier, Video Amplifier, Isolation Amplifier, Opto-couplers and fibre optic IC.

**TOTAL:45 PERIODS****OUTCOMES:**

Upon completion of the course, the student should be able to:

- Design linear and non linear applications of OP – AMPS
- Design applications using analog multiplier and PLL
- Design ADC and DAC using OP – AMPS
- Generate waveforms using OP – AMP Circuits
- Analyze special function ICs


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**OBJECTIVES:**

The student should be made to:

- Understand the division of network functionalities into layers.
- Be familiar with the components required to build different types of networks
- Be exposed to the required functionality at each layer
- Learn the flow control and congestion control algorithms

**UNIT I FUNDAMENTALS & LINK LAYER 9**

Overview of Data Communications- Networks – Building Network and its types– Overview of Internet - Protocol Layering - OSI Mode – Physical Layer – Overview of Data and Signals - introduction to Data Link Layer - Link layer Addressing- Error Detection and Correction

**UNIT II MEDIA ACCESS & INTERNETWORKING 9**

Overview of Data link Control and Media access control - Ethernet (802.3) - Wireless LANs – Available Protocols – Bluetooth – Bluetooth Low Energy – WiFi – 6LowPAN–Zigbee - Network layer services – Packet Switching – IPV4 Address – Network layer protocols ( IP, ICMP, Mobile IP)

**UNIT III ROUTING 9**

Routing - Unicast Routing – Algorithms – Protocols – Multicast Routing and its basics – Overview of Intradomain and interdomain protocols – Overview of IPv6 Addressing – Transition from IPv4 to IPv6

**UNIT IV TRANSPORT LAYER 9**

Introduction to Transport layer –Protocols- User Datagram Protocols (UDP) and Transmission Control Protocols (TCP) –Services – Features – TCP Connection – State Transition Diagram – Flow, Error and Congestion Control - Congestion avoidance (DECbit, RED) – QoS – Application requirements

**UNIT V APPLICATION LAYER 9**

Application Layer Paradigms – Client Server Programming – World Wide Web and HTTP - DNS- - Electronic Mail (SMTP, POP3, IMAP, MIME) – Introduction to Peer to Peer Networks – Need for Cryptography and Network Security – Firewalls.

**TOTAL:45 PERIODS****OUTCOMES:**

At the end of the course, the student should be able to:

- Identify the components required to build different types of networks
- Choose the required functionality at each layer for given application
- Identify solution for each functionality at each layer
- Trace the flow of information from one node to another node in the network

**TEXT BOOK:**

1. Behrouz A. Forouzan, "Data communication and Networking", Fifth Edition, Tata McGraw – Hill, 2013 (UNIT I –V)

**REFERENCES**

1. James F. Kurose, Keith W. Ross, "Computer Networking - A Top-Down Approach Featuring the Internet", Seventh Edition, Pearson Education, 2016.
2. Nader. F. Mir, " Computer and Communication Networks", Pearson Prentice Hall Publishers, 2<sup>nd</sup> Edition, 2014.
3. Ying-Dar Lin, Ren-Hung Hwang, Fred Baker, "Computer Networks: An Open Source Approach", Mc Graw Hill Publisher, 2011.
4. Larry L. Peterson, Bruce S. Davie, "Computer Networks: A Systems Approach", Fifth Edition, Morgan Kaufmann Publishers, 2011.

EC8691

**MICROPROCESSORS AND MICROCONTROLLERS**

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**OBJECTIVES:**

- To understand the Architecture of 8086 microprocessor.
- To learn the design aspects of I/O and Memory Interfacing circuits.
- To interface microprocessors with supporting chips.
- To study the Architecture of 8051 microcontroller.
- To design a microcontroller based system

**UNIT I THE 8086 MICROPROCESSOR 9**

Introduction to 8086 – Microprocessor architecture – Addressing modes - Instruction set and assembler directives – Assembly language programming – Modular Programming - Linking and Relocation - Stacks - Procedures – Macros – Interrupts and interrupt service routines – Byte and String Manipulation.

**UNIT II 8086 SYSTEM BUS STRUCTURE 9**

8086 signals – Basic configurations – System bus timing –System design using 8086 – I/O programming – Introduction to Multiprogramming – System Bus Structure – Multiprocessor configurations – Coprocessor, Closely coupled and loosely Coupled configurations – Introduction to advanced processors.

**UNIT III I/O INTERFACING 9**

Memory Interfacing and I/O interfacing - Parallel communication interface – Serial communication interface – D/A and A/D Interface - Timer – Keyboard /display controller – Interrupt controller – DMA controller – Programming and applications Case studies: Traffic Light control, LED display , LCD display, Keyboard display interface and Alarm Controller.

**UNIT IV MICROCONTROLLER 9**

Architecture of 8051 – Special Function Registers(SFRs) - I/O Pins Ports and Circuits - Instruction set - Addressing modes - Assembly language programming.

**UNIT V INTERFACING MICROCONTROLLER 9**


Programming 8051 Timers - Serial Port Programming - Interrupts Programming – LCD & Keyboard Interfacing - ADC, DAC & Sensor Interfacing - External Memory Interface- Stepper Motor and Waveform generation - Comparison of Microprocessor, Microcontroller, PIC and ARM processors.

**TOTAL: 45 PERIODS**

**OUTCOMES:**

**At the end of the course, the students should be able to:**

- Understand and execute programs based on 8086 microprocessor.
- Design Memory Interfacing circuits.
- Design and interface I/O circuits.
- Design and implement 8051 microcontroller based systems.

  
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**OBJECTIVES:**

The student should be made to:

- Understand the concepts of embedded system design and analysis
- Learn the architecture and programming of ARM processor
- Be exposed to the basic concepts of embedded programming
- Learn the real time operating systems

**UNIT I INTRODUCTION TO EMBEDDED SYSTEM DESIGN 9**

Complex systems and micro processors– Embedded system design process –Design example: Model train controller- Design methodologies- Design flows - Requirement Analysis – Specifications-System analysis and architecture design – Quality Assurance techniques - Designing with computing platforms – consumer electronics architecture – platform-level performance analysis.

**UNIT II ARM PROCESSOR AND PERIPHERALS 9**

ARM Architecture Versions – ARM Architecture – Instruction Set – Stacks and Subroutines – Features of the LPC 214X Family – Peripherals – The Timer Unit – Pulse Width Modulation Unit – UART – Block Diagram of ARM9 and ARM Cortex M3 MCU.

**UNIT III EMBEDDED PROGRAMMING 9**

Components for embedded programs- Models of programs- Assembly, linking and loading – compilation techniques- Program level performance analysis – Software performance optimization – Program level energy and power analysis and optimization – Analysis and optimization of program size- Program validation and testing.

**UNIT IV REAL TIME SYSTEMS 9**

Structure of a Real Time System — Estimating program run times – Task Assignment and Scheduling – Fault Tolerance Techniques – Reliability, Evaluation – Clock Synchronisation.

**UNIT V PROCESSES AND OPERATING SYSTEMS 9**

Introduction – Multiple tasks and multiple processes – Multirate systems- Preemptive real-time operating systems- Priority based scheduling- Interprocess communication mechanisms – Evaluating operating system performance- power optimization strategies for processes – Example Real time operating systems-POSIX-Windows CE. - Distributed embedded systems – MPSoCs and shared memory multiprocessors. – Design Example - Audio player, Engine control unit – Video accelerator.

**TOTAL: 45 PERIODS**

**OUTCOMES:**

At the end of the course, the student should be able to:

- Describe the architecture and programming of ARM processor
- Outline the concepts of embedded systems
- Explain the basic concepts of real time operating system design
- Model real-time applications using embedded-system concepts

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<b>EC8702</b>	<b>AD HOC AND WIRELESS SENSOR NETWORKS</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
		<b>3</b>	<b>0</b>	<b>0</b>	<b>3</b>

**OBJECTIVES:**

The student should be made to:

- Learn Ad hoc network and Sensor Network fundamentals
- Understand the different routing protocols
- Have an in-depth knowledge on sensor network architecture and design issues
- Understand the transport layer and security issues possible in Ad hoc and Sensor networks
- Have an exposure to mote programming platforms and tools

**UNIT I AD HOC NETWORKS – INTRODUCTION AND ROUTING PROTOCOLS 9**

Elements of Ad hoc Wireless Networks, Issues in Ad hoc wireless networks, Example commercial applications of Ad hoc networking, Ad hoc wireless Internet, Issues in Designing a Routing Protocol for Ad Hoc Wireless Networks, Classifications of Routing Protocols, Table Driven Routing Protocols - Destination Sequenced Distance Vector (DSDV), On-Demand Routing protocols –Ad hoc On-Demand Distance Vector Routing (AODV).

**UNIT II SENSOR NETWORKS – INTRODUCTION & ARCHITECTURES 9**

Challenges for Wireless Sensor Networks, Enabling Technologies for Wireless Sensor Networks, WSN application examples, Single-Node Architecture - Hardware Components, Energy Consumption of Sensor Nodes, Network Architecture - Sensor Network Scenarios, Transceiver Design Considerations, Optimization Goals and Figures of Merit.

**UNIT III WSN NETWORKING CONCEPTS AND PROTOCOLS 9**

MAC Protocols for Wireless Sensor Networks, Low Duty Cycle Protocols And Wakeup Concepts - S-MAC, The Mediation Device Protocol, Contention based protocols - PAMAS, Schedule based protocols – LEACH, IEEE 802.15.4 MAC protocol, Routing Protocols-Energy Efficient Routing, Challenges and Issues in Transport layer protocol.

**UNIT IV SENSOR NETWORK SECURITY 9**

Network Security Requirements, Issues and Challenges in Security Provisioning, Network Security Attacks, Layer wise attacks in wireless sensor networks, possible solutions for jamming, tampering, black hole attack, flooding attack. Key Distribution and Management, Secure Routing – SPINS, reliability requirements in sensor networks.

**UNIT V SENSOR NETWORK PLATFORMS AND TOOLS 9**

Sensor Node Hardware – Berkeley Motes, Programming Challenges, Node-level software platforms – TinyOS, nesC, CONTIKIOS, Node-level Simulators – NS2 and its extension to sensor networks, COOJA, TOSSIM, Programming beyond individual nodes – State centric programming.

**TOTAL:45 PERIODS**

**OUTCOMES:**

At the end of the course, the student would be able to:

- Know the basics of Ad hoc networks and Wireless Sensor Networks
- Apply this knowledge to identify the suitable routing algorithm based on the network and user requirement
- Apply the knowledge to identify appropriate physical and MAC layer protocols
- Understand the transport layer and security issues possible in Ad hoc and sensor networks.
- Be familiar with the OS used in Wireless Sensor Networks and build basic modules

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EC8094

SATELLITE COMMUNICATION

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**OBJECTIVES:**

The student should be made to:

- Understand the basics of satellite orbits
- Understand the satellite segment and earth segment
- Analyze the various methods of satellite access
- Understand the applications of satellites
- Understand the basics of satellite Networks

**UNIT I SATELLITE ORBITS**

9

Kepler's Laws, Newton's law, orbital parameters, orbital perturbations, station keeping, geo stationary and non Geo-stationary orbits – Look Angle Determination- Limits of visibility – eclipse-Sub satellite point –Sun transit outage-Launching Procedures - launch vehicles and propulsion.

**UNIT II SPACE SEGMENT**

9

Spacecraft Technology- Structure, Primary power, Attitude and Orbit control, Thermal control and Propulsion, communication Payload and supporting subsystems, Telemetry, Tracking and command-Transponders-The Antenna Subsystem.

**UNIT III SATELLITE LINK DESIGN**

9

Basic link analysis, Interference analysis, Rain induced attenuation and interference, Ionospheric characteristics, Link Design with and without frequency reuse.

**UNIT IV SATELLITE ACCESS AND CODING METHODS**

9

Modulation and Multiplexing: Voice, Data, Video, Analog – digital transmission system, Digital video Broadcast, multiple access: FDMA, TDMA, CDMA, DAMA Assignment Methods, compression – encryption, Coding Schemes.

**UNIT V SATELLITE APPLICATIONS**

9

INTELSAT Series, INSAT, VSAT, Mobile satellite services: GSM, GPS, INMARSAT, LEO, MEO, Satellite Navigational System. GPS Position Location Principles, Differential GPS, Direct Broadcast satellites (DBS/DTH).

**TOTAL:45 PERIODS**

**OUTCOMES:**

At the end of the course, the student would be able to:

- Analyze the satellite orbits
- Analyze the earth segment and space segment
- Analyze the satellite Link design
- Design various satellite applications

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**COURSE OBJECTIVES:**

- . To understand the basics of algorithmic problem solving.
- . To learn to solve problems using Python conditionals and loops.
- . To define Python functions and use function calls to solve problems.
- . To use Python data structures - lists, tuples, dictionaries to represent complex data.
- . To do input/output with files in Python.

**UNIT I COMPUTATIONAL THINKING AND PROBLEM SOLVING 9**

Fundamentals of Computing – Identification of Computational Problems -Algorithms, building blocks of algorithms (statements, state, control flow, functions), notation (pseudo code, flow chart, programming language), algorithmic problem solving, simple strategies for developing algorithms (iteration, recursion). Illustrative problems: find minimum in a list, insert a card in a list of sorted cards, guess an integer number in a range, Towers of Hanoi.

**UNIT II DATA TYPES, EXPRESSIONS, STATEMENTS 9**

Python interpreter and interactive mode,debugging; values and types: int, float, boolean, string, and list; variables, expressions, statements, tuple assignment, precedence of operators, comments; Illustrative programs: exchange the values of two variables, circulate the values of n variables, distance between two points.

**UNIT III CONTROL FLOW, FUNCTIONS, STRINGS 9**

Conditionals: Boolean values and operators, conditional (if), alternative (if-else), chained conditional (if-elif-else); Iteration: state, while, for, break, continue, pass; Fruitful functions: return values, parameters, local and global scope, function composition, recursion; Strings: string slices, immutability, string functions and methods, string module; Lists as arrays. Illustrative programs: square root, gcd, exponentiation, sum an array of numbers, linear search, binary search.

**UNIT IV LISTS, TUPLES, DICTIONARIES 9**


Lists: list operations, list slices, list methods, list loop, mutability, aliasing, cloning lists, list parameters; Tuples: tuple assignment, tuple as return value; Dictionaries: operations and methods; advanced list processing - list comprehension; Illustrative programs: simple sorting, histogram, Students marks statement, Retail bill preparation.

**UNIT V FILES, MODULES, PACKAGES 9**

Files and exceptions: text files, reading and writing files, format operator; command line arguments, errors and exceptions, handling exceptions, modules, packages; illustrative programs: word count, copy file, Voter's age validation, Marks range validation (0-100).

**TOTAL : 45 PERIODS****COURSE OUTCOMES:**

- Upon completion of the course, students will be able to
- CO1:** Develop algorithmic solutions to simple computational problems.
  - CO2:** Develop and execute simple Python programs.
  - CO3:** Write simple Python programs using conditionals and loops for solving problems.
  - CO4:** Decompose a Python program into functions.
  - CO5:** Represent compound data using Python lists, tuples, dictionaries etc.
  - CO6:** Read and write data from/to files in Python programs.

  
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**COURSE OBJECTIVES :**

- To give a comprehensive exposure to all types of devices and circuits constructed with discrete components. This helps to develop a strong basis for building linear and digital integrated circuits
- To analyze the frequency response of small signal amplifiers
- To design and analyze single stage and multistage amplifier circuits
- To study about feedback amplifiers and oscillators principles
- To understand the analysis and design of multi vibrators

**UNIT I SEMICONDUCTOR DEVICES**

9

PN junction diode, Zener diode, BJT, MOSFET, UJT –structure, operation and V-I characteristics, diffusion and transition capacitance - Rectifiers – Half Wave and Full Wave Rectifier, Zener as regulator

**UNIT II AMPLIFIERS**

9

Load line, operating point, biasing methods for BJT and MOSFET, BJT small signal model – Analysis of CE, CB, CC amplifiers- Gain and frequency response –MOSFET small signal model– Analysis of CS, CG and Source follower – Gain and frequency response- High frequency analysis.

**UNIT III MULTISTAGE AMPLIFIERS AND DIFFERENTIAL AMPLIFIER**

9

Cascode amplifier, Differential amplifier – Common mode and Difference mode analysis – MOSFET input stages – tuned amplifiers – Gain and frequency response – Neutralization methods.

**UNIT IV FEEDBACK AMPLIFIERS AND OSCILLATORS**

9

Advantages of negative feedback – Voltage / Current, Series , Shunt feedback Amplifiers – positive feedback–Condition for oscillations, phase shift – Wien bridge, Hartley, Colpitts and Crystal oscillators.

**UNIT V POWER AMPLIFIERS AND DC/DC CONVERTERS**

9

Power amplifiers- class A-Class B-Class AB-Class C-Power MOSFET-Temperature Effect- Class AB Power amplifier using MOSFET –DC/DC convertors – Buck, Boost, Buck-Boost analysis and design.

**TOTAL: 45 PERIODS****COURSE OUTCOMES :**

At the end of the course the students will be able to

**CO1:** Explain the structure and working operation of basic electronic devices.

**CO2:** Design and analyze amplifiers.

**CO3:** Analyze frequency response of BJT and MOSFET amplifiers

**CO4:** Design and analyze feedback amplifiers and oscillator principles.

**CO5:** Design and analyze power amplifiers and supply circuits

**TEXT BOOKS :**

1. David A. Bell, "Electronic Devices and Circuits", Oxford Higher Education press, 5 th Edition, 2010.
2. Robert L. Boylestad and Louis Nasheresky, "Electronic Devices and Circuit Theory", 10th Edition, Pearson Education / PHI, 2008.
3. Adel .S. Sedra, Kenneth C. Smith, "Micro Electronic Circuits", Oxford University Press, 7 th Edition, 2014.


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**COURSE OBJECTIVES :**

- Learn the architecture and features of 8051.
- Study the design process of an embedded system.
- Understand the real – time processing in an embedded system.
- Learn the architecture and design flow of IoT.
- Build an IoT based system.

**UNIT I 8051 MICROCONTROLLER**

9

Microcontrollers for an Embedded System – 8051 – Architecture – Addressing Modes – Instruction Set – Program and Data Memory – Stacks – Interrupts – Timers/Counters – Serial Ports – Programming.

**UNIT II EMBEDDED SYSTEMS**

9

Embedded System Design Process – Model Train Controller – ARM Processor – Instruction Set Preliminaries – CPU – Programming Input and Output – Supervisor Mode – Exceptions and Trap – Models for programs – Assembly, Linking and Loading – Compilation Techniques – Program Level Performance Analysis.

**UNIT III PROCESSES AND OPERATING SYSTEMS**

9

Structure of a real – time system – Task Assignment and Scheduling – Multiple Tasks and Multiple Processes – Multirate Systems – Pre emptive real – time Operating systems – Priority based scheduling – Interprocess Communication Mechanisms – Distributed Embedded Systems – MPSoCs and Shared Memory Multiprocessors – Design Example – Audio Player, Engine Control Unit and Video Accelerator.

**UNIT IV IOT ARCHITECTURE AND PROTOCOLS**

9

Internet – of – Things – Physical Design, Logical Design – IoT Enabling Technologies – Domain Specific IoTs – IoT and M2M – IoT System Management with NETCONF – YANG – IoT Platform Design – Methodology – IoT Reference Model – Domain Model – Communication Model – IoT Reference Architecture – IoT Protocols - MQTT, XMPP, Modbus, CANBUS and BACNet.

**UNIT V IOT SYSTEM DESIGN**

9

Basic building blocks of an IoT device – Raspberry Pi – Board – Linux on Raspberry Pi – Interfaces – Programming with Python – Case Studies: Home Automation, Smart Cities, Environment and Agriculture.

45 PERIODS

**PRACTICAL EXERCISES**

30 PERIODS

Experiments using 8051.

1. Programming Arithmetic and Logical Operations in 8051.
2. Generation of Square waveform using 8051.
3. Programming using On – Chip ports in 8051.
4. Programming using Serial Ports in 8051.
5. Design of a Digital Clock using Timers/Counters in 8051.

Experiments using ARM

Interfacing ADC and DAC

Blinking of LEDs and LCD

Interfacing keyboard and Stepper Motor.



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Miniprojects for IoT  
 Garbage Segregator and Bin Level Indicator  
 Colour based Product Sorting  
 Image Processing based Fire Detection  
 Vehicle Number Plate Detection  
 Smart Lock System

**TOTAL: 60 PERIODS**

**COURSE OUTCOMES:**

- CO1:** Explain the architecture and features of 8051.  
**CO2:** Develop a model of an embedded system.  
**CO3:** List the concepts of real time operating systems.  
**CO4:** Learn the architecture and protocols of IoT.  
**CO5:** Design an IoT based system for any application.

**TEXTBOOKS :**

1. Mohammed Ali Mazidi, Janice Gillispie Mazidi, Rolin D.McKinlay, The 8051 Microcontroller and Embedded Systems Using Assembly and C, Second Edition, Pearson Education, 2008.(Unit – I)
2. Marilyn Wolf, Computers as Components – Principles of Embedded Computing System Design, Third Edition, Morgan Kaufmann, 2012.(Unit – II,III)
3. Arshdeep Bahga, Vijay Madisetti, Internet – of- Things – A Hands on Approach, Universities Press, 2015.(Unit – IV,V)

**REFERENCES :**

1. Mayur Ramgir, Internet – of – Things, Architecture, Implementation and Security, First Edition, Pearson Education, 2020.
2. Lyla B.Das, Embedded Systems: An Integrated Approach, Pearson Education 2013.
3. Jane.W.S .Liu, Real – Time Systems, Pearson Education, 2003.

**CO's-PO's & PSO's MAPPING**

C	PO	PO	PO	PO	PO	PO	PO	PO	PO	PO1	PO1	PO1	PSO	PSO	PSO
1	3	3	3	2	2	-	-	-	-	-	-	-	3	2	1
2	3	3	3	2	2	-	-	-	-	-	-	-	3	2	1
3	3	3	2	2	2	-	-	-	-	-	-	-	2	1	1
4	3	3	2	2	2	-	-	-	-	-	-	-	3	3	2
5	3	3	3	3	3	-	-	-	-	-	-	-	3	3	2
<b>C</b>	<b>3</b>	<b>3</b>	<b>2.6</b>	<b>2.2</b>	<b>2.2</b>	-	-	-	-	-	-	-	<b>2.8</b>	<b>2.2</b>	<b>1.4</b>

1 - low, 2 - medium, 3 - high, '-' - no correlation

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**COURSE OBJECTIVES:**

- To understand the basics of IoT.
- To get knowledge about the various services provided by IoT.
- To familiarize themselves with various communication techniques and networking.
- To know the implementation of IoT with different tools.
- To understand the various applications in IoT.

**UNIT I INTRODUCTION TO INTERNET OF THINGS**

9

Rise of the machines – Evolution of IoT – Web 3.0 view of IoT – Definition and characteristics of IoT – IoT Enabling Technologies – IoT Architecture – Fog, Edge and Cloud in IoT – Functional blocks of an IoT ecosystem – Sensors, Actuators, Smart Objects and Connecting Smart Objects – IoT levels and deployment templates – A panoramic view of IoT applications.

**UNIT II MIDDLEWARE AND PROTOCOLS OF IOT**

9

Middleware technologies for IoT system (IoT Ecosystem Overview – Horizontal Architecture Approach for IoT Systems – SOA based IoT Middleware) Middleware architecture of RFID, WSN, SCADA, M2M – Interoperability challenges of IoT-Protocols for RFID, WSN, SCADA, M2M- Zigbee, KNX, BACNet, MODBUS - Challenges Introduced by 5G in IoT Middleware (Technological Requirements of 5G Systems - Perspectives and a Middleware Approach Toward 5G (COMPaaS Middleware) – Resource management in IoT.

**UNIT III COMMUNICATION AND NETWORKING**

9

IoT Access Technologies: Physical and MAC layers, topology and Security of IEEE 802.15.4, 802.15.4g, 802.15.4e, 1901.2a, 802.11ah and LoRaWAN – Network Layer: IP versions, Constrained Nodes and Constrained Networks – Optimizing IP for IoT: From 6LoWPAN to 6Lo, Routing over Low Power and Lossy Networks – Application Transport Methods: Supervisory Control and Data Acquisition – Application Layer Protocols: CoAP and MQTT- Data aggregation & dissemination.

**UNIT IV IOT IMPLEMENTATION TOOLS**

9

Introduction to Python, Introduction to different IoT tools, Developing applications through IoT tools, Developing sensor based application through embedded system platform, Implementing IoT concepts with python, Implementation of IoT with Raspberry Pi.

**UNIT V APPLICATIONS AND CASE STUDIES:**

9

Home automations - Smart cities – Environment – Energy – Retail – Logistics – Agriculture – Industry - Health and life style – Case study.

**TOTAL: 45 PERIODS****COURSE OUTCOMES:**

Upon completion of this course, the students will be able to

**CO1:** Articulate the main concepts, key technologies, strength and limitations of IoT.

**CO2:** Identify the architecture, infrastructure models of IoT.

**CO3:** Analyze the networking and how the sensors are communicated in IoT .

**CO4:** Analyze and design different models for IoT implementation.

**CO5:** Identify and design the new models for market strategic interaction.

**Principal**

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